

OET004 – MICROPROCESSORS/MICROCONTROLLERS TAG RUBRIC**November 2019****Credit Hours:** 3-4 Semester Hours**Prerequisite:** Digital Electronics (OET002)**Related TAG:** Electrical Engineering Technology

General Course Description: This course includes microprocessor/microcontroller architecture, instruction sets, software development, interrupt handling, memory, interfacing techniques, and hardware used in control applications designed with microprocessor/ microcontrollers. Includes hands-on labs.

Student learning outcomes marked with an asterisk (*) are essential and must be met.

| TAG Learning Outcomes | Applied skills strongly demonstrated | Some applied skills present | Little applied skills present | No applied skills present |
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| 1. Explain microprocessor architecture.* | <ul style="list-style-type: none">Describe the steps in executing an instruction and how each part of the architecture interreacts for the key parts and programmer accessible CPU registers. | | <ul style="list-style-type: none">Explain the function of the key parts.Explain the purpose of each programmer accessible CPU register. | <ul style="list-style-type: none">Describe the key parts of a Microprocessor architecture- CPU, memory, three busses and ports.State the programmer accessible CPU registers. |
| 2. Utilize assembly language programming to develop code for a microprocessor.* | <ul style="list-style-type: none">Design, code and test an assembly language program including iterationDesign, code and test an assembly language program that interacts with a device connected to a port.Design, code and test an assembly language, program using assembler directives. | <ul style="list-style-type: none">Design, code and test a simple straight-line assembly language program.Design, code and test an assembly language program including iteration.Describe assembler directives and what they are used for.Write an assembly language program using | <ul style="list-style-type: none">Translate machine instructions to assembler instructions and vice versa.Assemble, download and run an existing program.Explain the function of the stack and subroutines.State several ways to create needed timing | <ul style="list-style-type: none">Explain what assembly language is and how it relates to machine language.Explain what an assembler is.Define stack and subroutines and why they are important.Explain why time consideration is important in |

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| | <ul style="list-style-type: none"> • Design, code and test an assembly language program using the stack and subroutines. • Design, code and test an assembly language program using time delays. • Download and debug an assembly language using interrupts. | <p>the stack and subroutines</p> <ul style="list-style-type: none"> • Write an assembly language program using programmer accessible CPU registers. • Write an assembly language program using time delays. • Write an assembly language program using interrupts. | <p>and what each might be used for.</p> <ul style="list-style-type: none"> • Explain why interrupts are important and what they are used for. | <p>Microcontroller programming.</p> <ul style="list-style-type: none"> • Describe what interrupts are. |
| 3. Explain and utilize bus timing diagrams.* | <ul style="list-style-type: none"> • Sketch a timing diagram for several assembly language instructions including the individual control signals. | <ul style="list-style-type: none"> • Identify and give the function for the individual control signals in the control bus. | | |
| 4. Demonstrate an understanding of and applications for bus structures.* | <ul style="list-style-type: none"> • Describe how each bus is used in executing a write-to-port instruction. | <ul style="list-style-type: none"> • Describe how each bus is used in an instruction fetch and execute. | <ul style="list-style-type: none"> • Describe the function of each bus. | <ul style="list-style-type: none"> • List the three main busses. |
| 5. Utilize memory technologies and interfacing in microprocessors.* | <ul style="list-style-type: none"> • Describe the sequence for writing and reading from a memory chip in terms of the memory connections. | <ul style="list-style-type: none"> • Describe the connections to a memory chip and what each does. | <ul style="list-style-type: none"> • Describe how each memory technology works and what each is typically used for. | <ul style="list-style-type: none"> • Identify the various memory technologies |
| 6. Implement input/output (I/O) systems, I/O interface requirements, and interrupt based I/O.* | <ul style="list-style-type: none"> • Test the program using lights and switches. • Debug using debugging tools in the monitor or operating system. • Download and test a program using interrupt based I/O. | <ul style="list-style-type: none"> • Assemble and debug at the assembler level a program that interfaces with lights and switches. • Write, assemble and debug at the assembler level a program using interrupt based I/O | <ul style="list-style-type: none"> • Code a program that interfaces with lights and switches. • Design a program using interrupt based I/O. | <ul style="list-style-type: none"> • Design a program that interfaces with lights and switches. • Explain how interrupt based I/O works. |

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| 7. Utilize direct memory access (DMA) in microprocessor applications.* | <ul style="list-style-type: none"> Design, build and test a system using DMA and DMA controller. | <ul style="list-style-type: none"> Describe the function of a DMA controller. | <ul style="list-style-type: none"> Identify the different approaches to DMA. | <ul style="list-style-type: none"> Describe the purpose of DMA. |
| 8. Utilize microprocessors/microcontrollers in a variety of applications.* | <ul style="list-style-type: none"> Test and debug a system using two or more peripherals. | <ul style="list-style-type: none"> Assemble and debug the code at the assembler level using two or more peripherals. | <ul style="list-style-type: none"> Code the program using two or more peripherals and assemble the electronic hardware. | <ul style="list-style-type: none"> Design a system using two or more peripherals at the block diagram and flow chart level |
| <i>Note that this TAG should be updated to include programming with the C language since most of the programming now is done with C or C-like languages.</i> | | | | |